



GifZUWY Acibh ; YbYfU` DifdcgY FYWhjZjYf

: YUhi fYg

- Low profile package
- Ideal for automated placement
- Glass passivated chip junction
- High forward surge capability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C

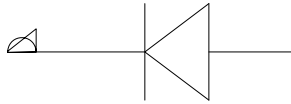
HmdjWU` 5dd`jWUh]cbg

For use in general purpose rectification of power supplies, inverters, converters, and freewheeling diodes for consumer and telecommunication.

AYW\UbjWU` 8UhU

rD^{3/4}fM-

DUW_U[Y: DO-214AA (SMB)
Molding compound meets UL 94 V-0 flammability rating, RoHS-compliant, halogen-free
HYf a]bU`g:



D5F5A9H9F	GMA6C@	IB=H	; G&5	; G&6	; G&8	; G&;	; G&>	; G&?	; G&A
Device marking code			GS2A	GS2B	GS2D	GS2G	GS2J	GS2K	GS2M
Maximum Repetitive peak reverse voltage	V _{RRM}	V	50	100	200	400	600	800	1000
Maximum RMS Voltage	V _{RMS}	V	35	70	140	280	420	560	700
Maximum DC Blocking Voltage @60Hz Half-sine wave, 1 cycle, T _j =25	V _{DC}	V	50	100	200	400	600	800	1000

Average rectified output current @60Hz sine wave, resistance load, TL (Fig.1 i ig.1

			50
Forward Surge Current (Non-repetitive) @1ms, square wave, 1 cycle, T _j =25	I _{FSM}	A	100
Current squared time @1ms t 8.3ms T _j =25	I ² t	A ² s	10.735
Storage temperature	T _{stg}		-55 ~ +150
Junction temperature	T _j		-55 ~ +150



; G&5 H<F I ; G&A`

9`YWhf]WU` 7 \UfUWhYf]gh]Wg Ta=25 Unless otherwise specified



CfXYf]b[-bZcf a Uh]cb (Example)

DF9:9F98 D#B	D57?-B ; 7C89	IB=H K9=; <Hfl[L	A=B=A I A D57?5 ; 9fidWgt	=BB9F 6CL E I 5BH=HMfidWgt	C I H9F 75FHCB E I 5BH=HMfidWgt	89@=-J9FM AC89
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8]gW'U]a Yf

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